

LOW POWER CURRENT MIRROR CIRCUIT

ABSTRACT OF THE DISCLOSURE

A current mirror circuit is provided. The circuit includes a resistor having a first terminal connected to a current source, a first transistor having a substrate electrode connected to a drain electrode thereof, a second transistor having a substrate electrode connected to the substrate electrode of the first transistor, a third transistor having a substrate electrode connected to the substrate electrode of the first transistor, and a fourth transistor having a drain electrode for providing an output current.